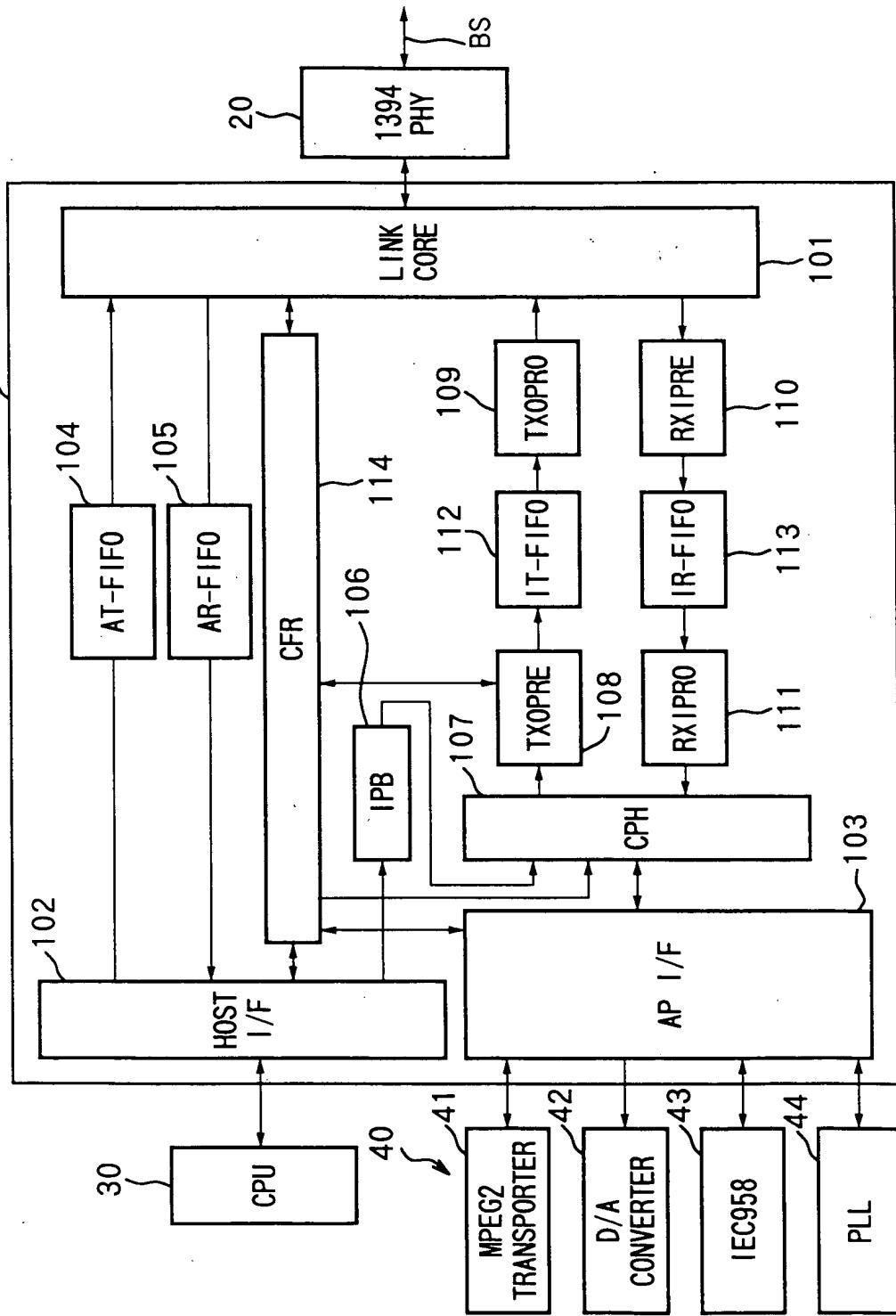


~99P1277
US00

FIG. 1



sy[3 : 2]	CIPHER MODE	CONTENTS
11	MODE A	Never Copy
10	MODE B	Copy Once
01	MODE C	No More Copy
00	NO ENCIPHER	Copy Free

FIG.2A

sy[1]	KIND OF CIPHER KEY
1	Even Key
0	Odd Key

FIG.2B

FIG. 3

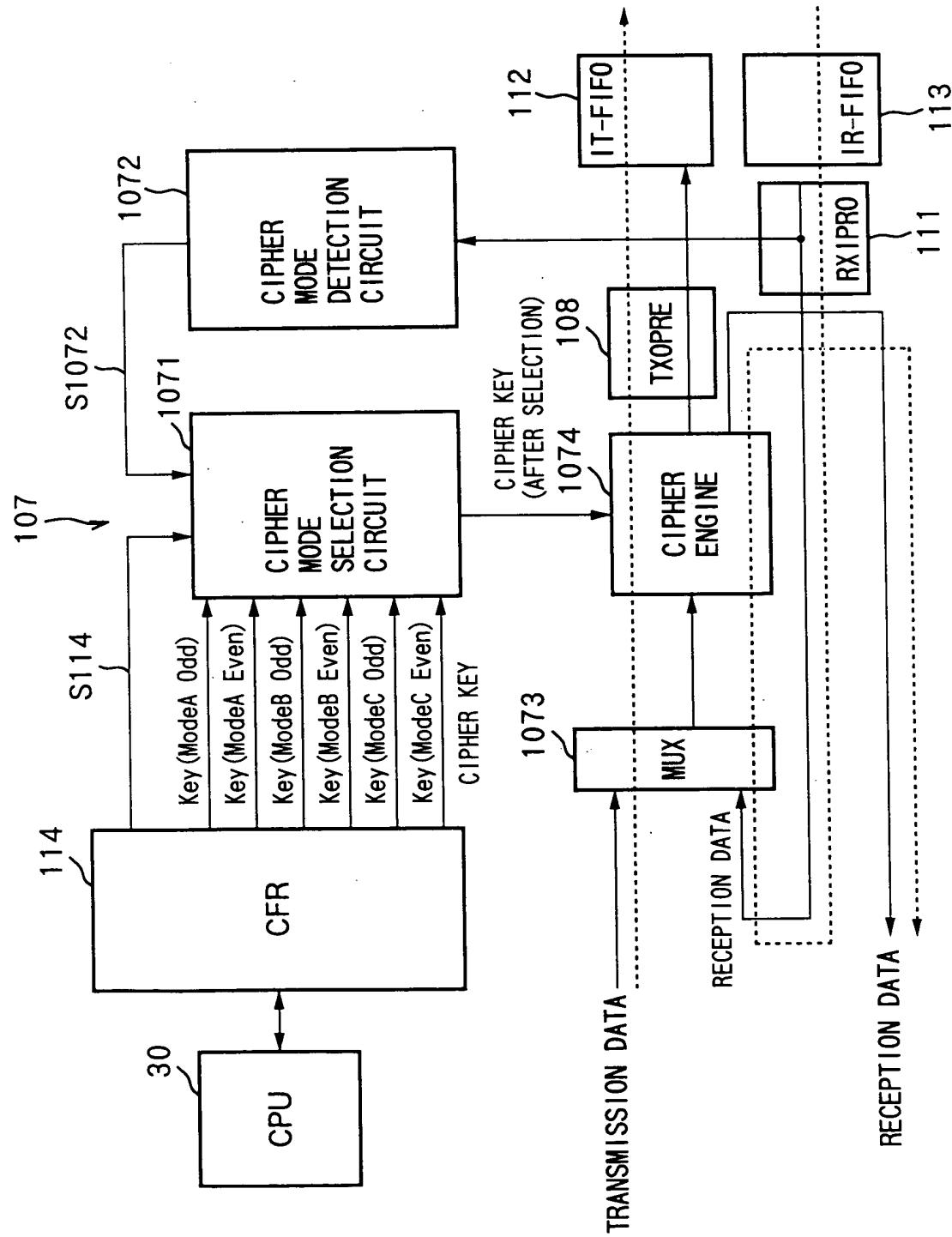


FIG. 4A

TimeStamp	sy[3:1]	CC	CO
35	34	33	32
34	33	32	31
33	32	31	30
30	29	28	27
29	28	27	26
28	27	26	25
27	26	25	24
26	25	24	23
25	24	23	22
24	23	22	21
23	22	21	20
22	21	20	19
21	20	19	18
20	19	18	17
19	18	17	16
18	17	16	15
17	16	15	14
16	15	14	13
15	14	13	12
14	13	12	11
13	12	11	10
12	11	10	9
11	10	9	8
10	9	8	7
9	8	7	6
8	7	6	5
7	6	5	4
6	5	4	3
5	4	3	2
4	3	2	1
3	2	1	0
2	1		
1			
0			

FIG. 4B

Data	sy[3:1]	DataPayload
35	34	33
34	33	32
33	32	31
32	31	30
31	30	29
29	28	27
28	27	26
27	26	25
26	25	24
25	24	23
24	23	22
23	22	21
22	21	20
21	20	19
20	19	18
19	18	17
18	17	16
17	16	15
16	15	14
15	14	13
14	13	12
13	12	11
12	11	10
11	10	9
10	9	8
9	8	7
8	7	6
7	6	5
6	5	4
5	4	3
4	3	2
3	2	1
2	1	0
1		
0		

FIG.5

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
DataLength																tag	channel	tcde	sy												

ASSIGN bit3-bit1 OF sy[3:0] TO CIPHER MODE

FIG.6

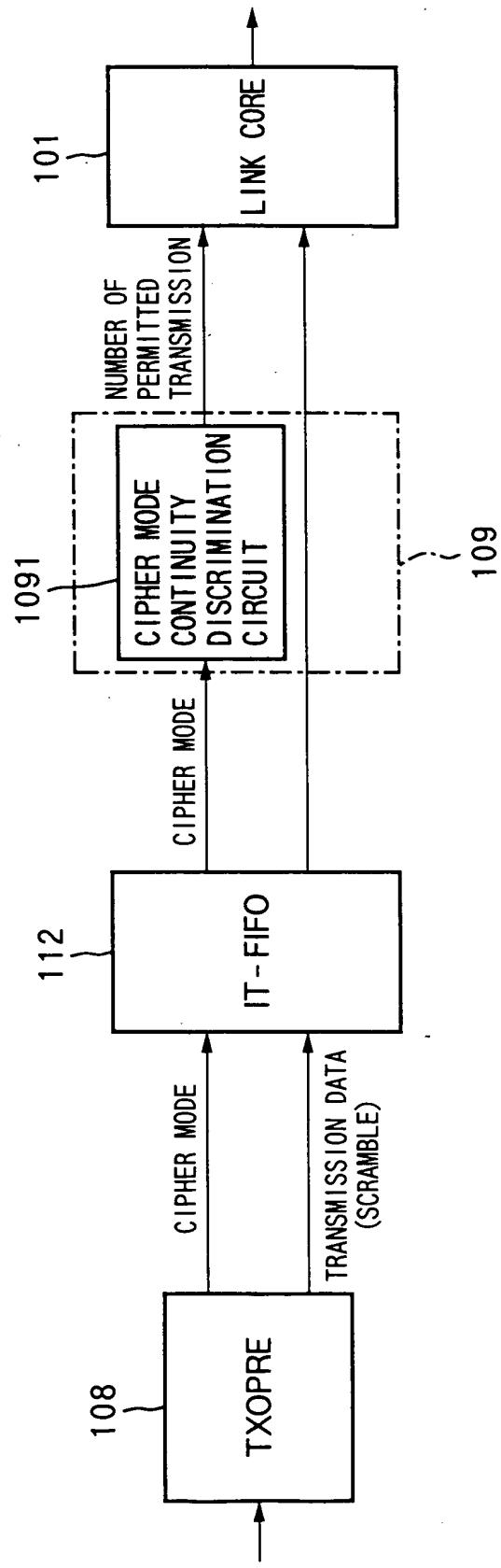
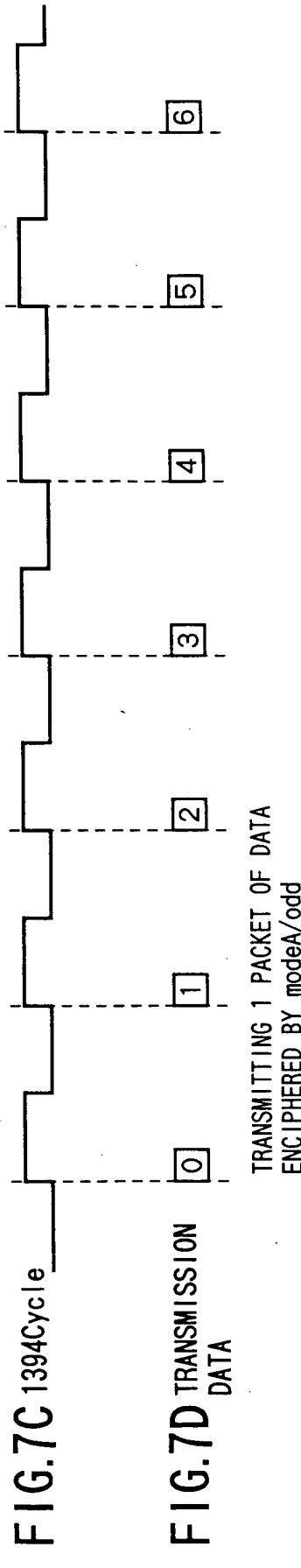
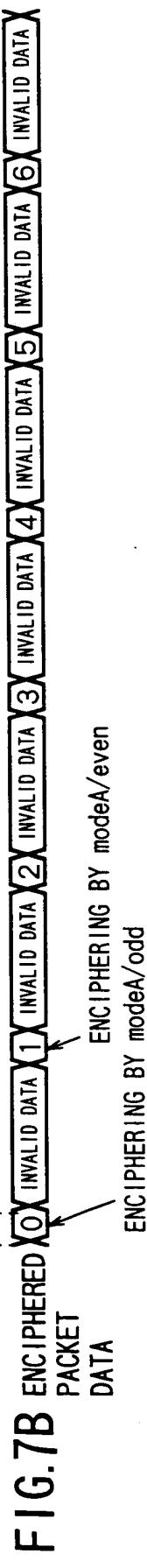


FIG. 7A CIPHER MODE

TRANSMITTING 1 PACKET OF DATA WITHIN 1 CYCLE



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FIG. 8A CIPHER MODE

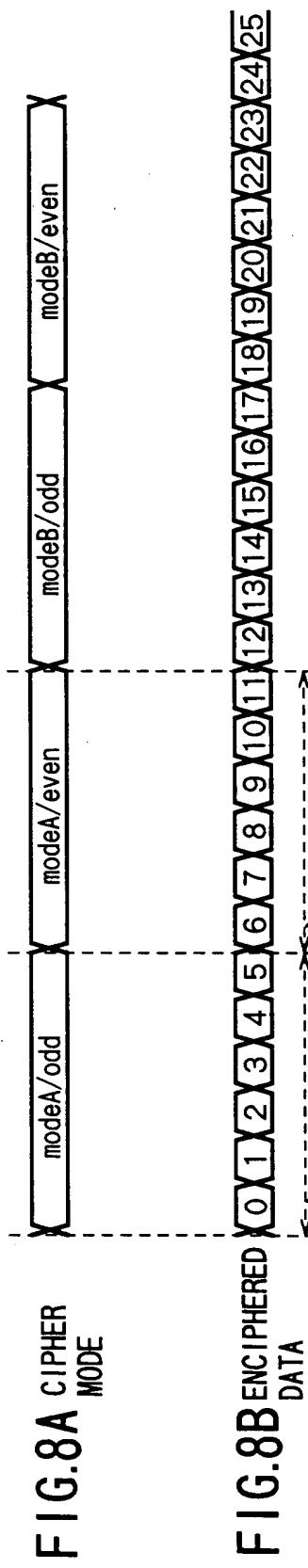


FIG. 8B ENCI
PHERED DATA

ENCIPHERING BY modeA/odd

ENCIPHERING BY modeA/even

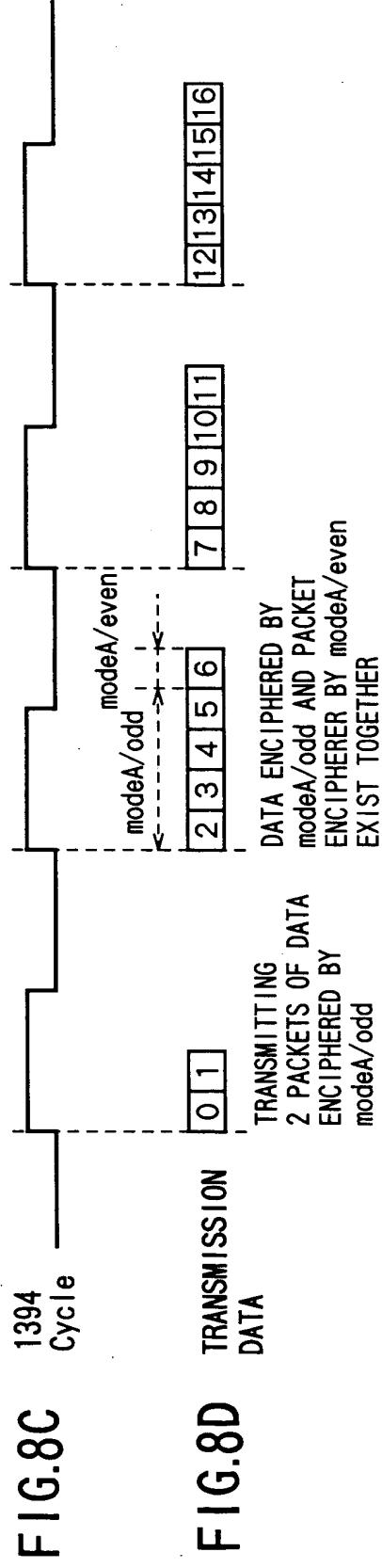


FIG. 9A CIPHER MODE



FIG. 9B ENCRYPTED DATA

ENCIPHERING BY
modeA/odd

ENCIPHERING BY
modeA/even



FIG. 9C TRANSMISSION DATA

TRANSMITTING
2 PACKETS OF DATA
ENCI PHERED BY
modeA/odd

0 1

2 3 4 5

6 7 8 9 10

11 12 13 14 15

TRANSMITTING
4 PACKETS OF DATA
ENCI PHERED BY
modeA/odd

TRANSMITTING
5 PACKETS OF DATA
ENCI PHERED BY
modeA/even

(5PACKETS CAN BE
TRANSMITTED IN THIS CYCLE,
HOWEVER, 4 PACKETS ARE
TRANSMITTED TO PREVENT
MIXTURE OF CIPHER MODES)

FIG. 10A
DVB

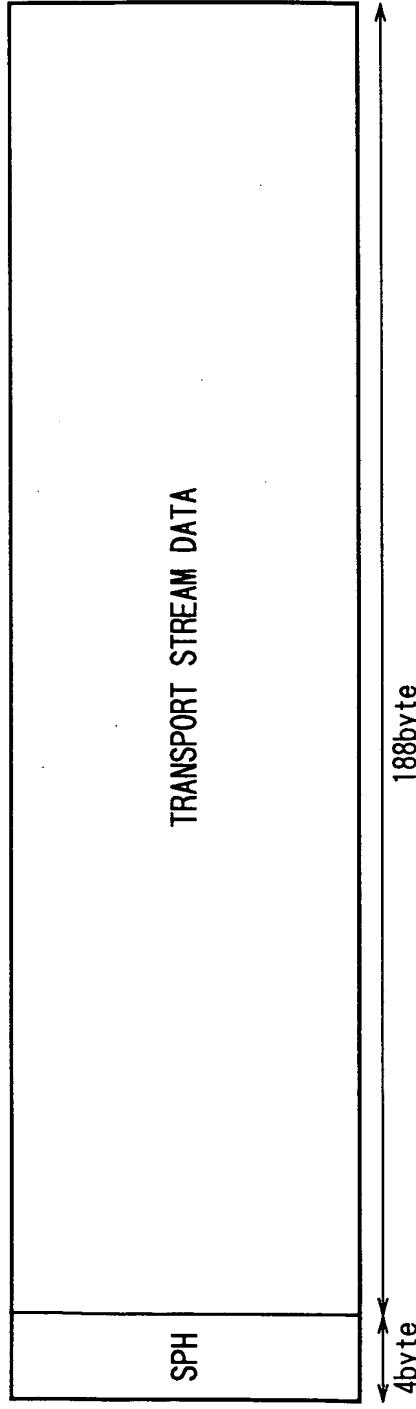


FIG. 10B
DSS

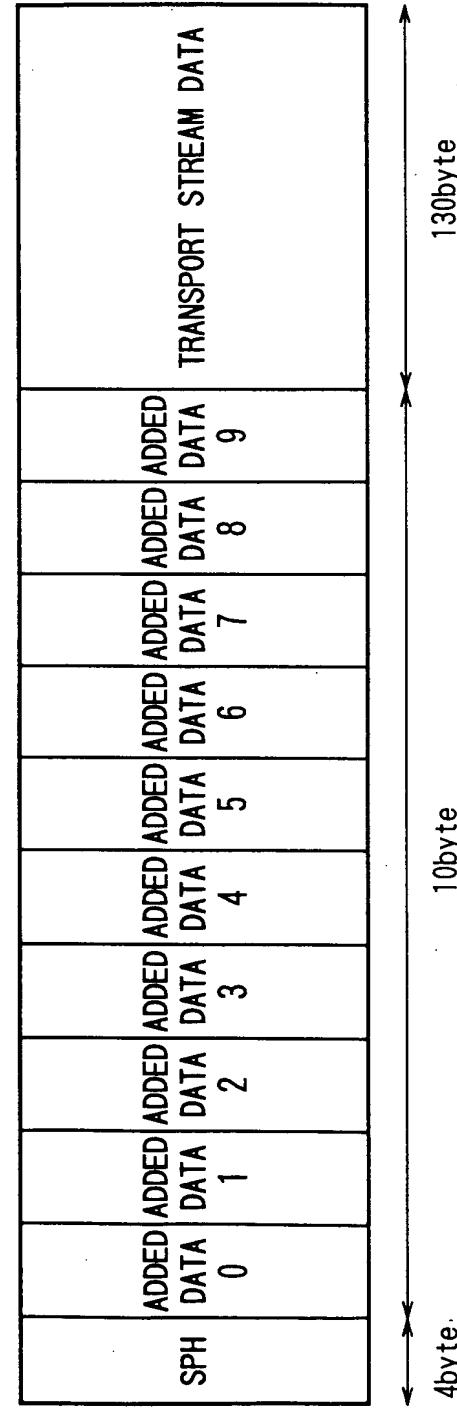


FIG. 11

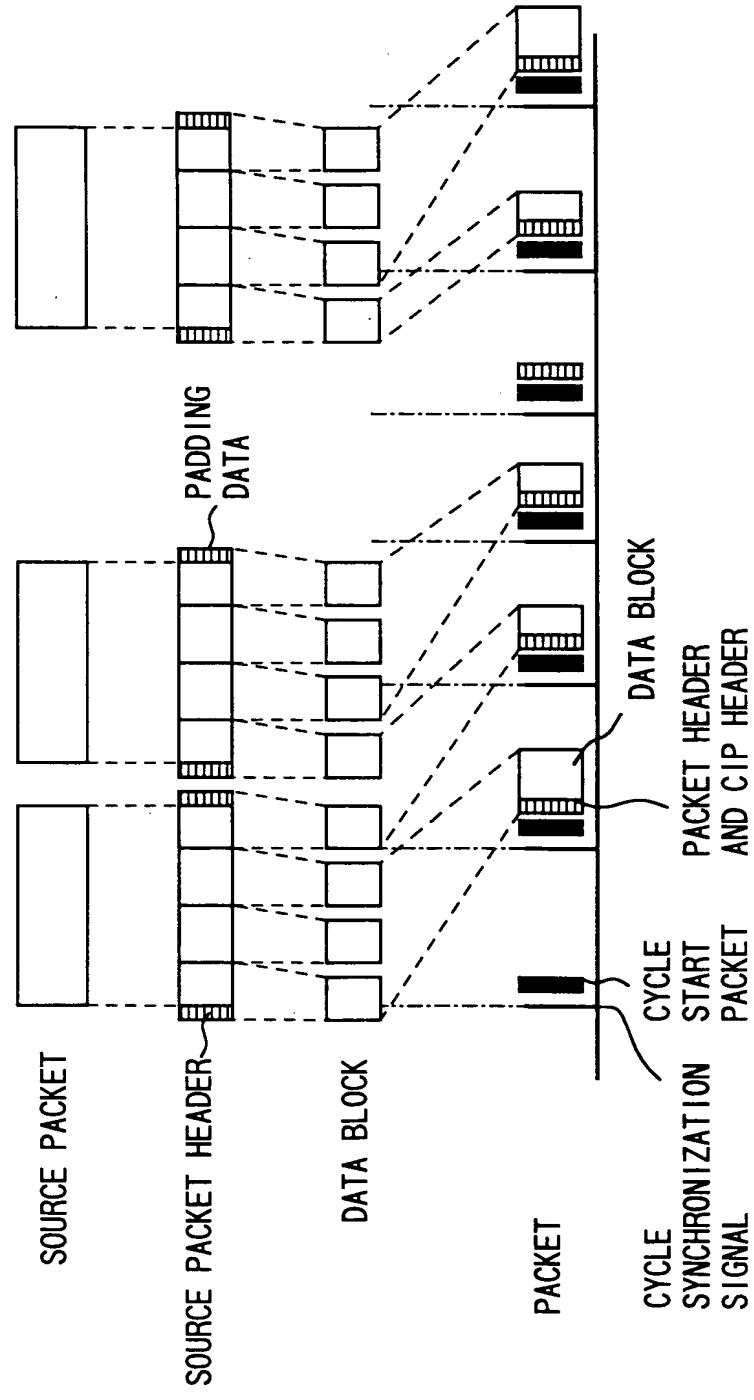
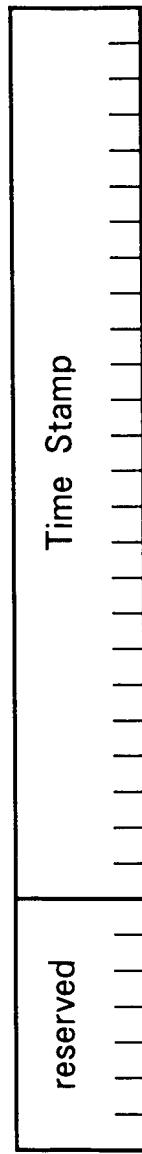


FIG. 12



SOURCE PACKET HEADER (SPH)

FIG. 13

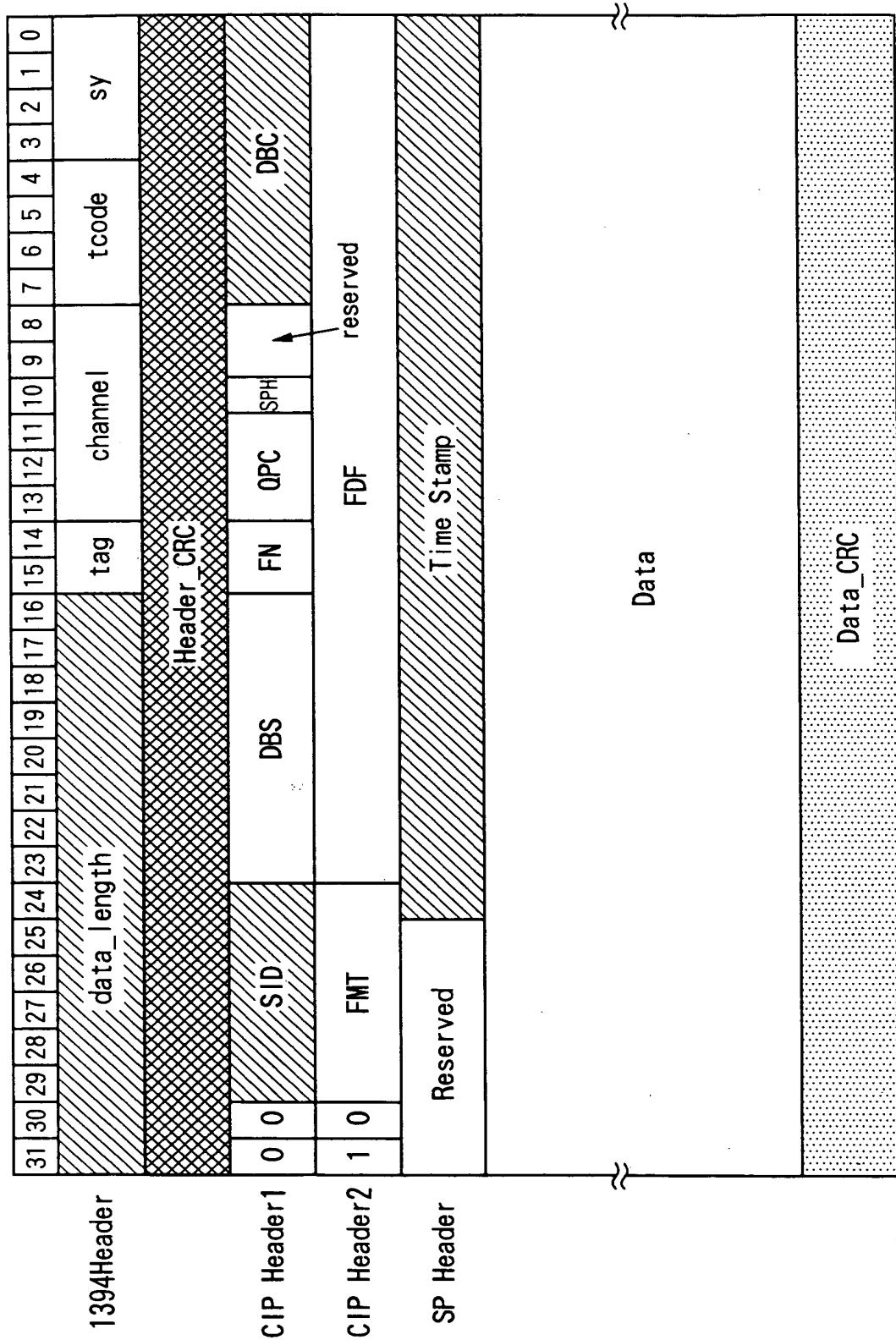


FIG. 14

IEEE1394BUS

